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#### **BOX PCT**

# IN THE UNITED STATES DESIGNATED/ELECTED OFFICE OF THE UNITED STATES PATENT AND TRADEMARK OFFICE UNDER THE PATENT COOPERATION TREATY-CHAPTER II

# INFORMATION DISCLOSURE STATEMENT ACCOMPANYING THE FILING OF AN APPLICATION

APPLICANT(S):

Udo Schwalke et al.

ATTORNEY DOCKET NO.:

P99,2666

INTERNATIONAL APPLICATION NO.:

PCT/DE98/01312

10 INTERNATIONAL FILING DATE:

11 May 1998

INVENTION:

"INTEGRATED CIRCUIT ARRANGEMENT AND METHOD FOR

THE MANUFACTURE THEREOF"

Assistant Commissioner for Patents Washington, D.C. 20231

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In accordance with the provisions of 37 C.F.R. § 1.56, Applicant requests that citation and examination of the following references be made during the course of examination of the above-referenced application for United States Letters Patent.

## I. SUBMITTED REFERENCES

20 AA United States Patent No. 5,416,343

AB United States Patent No. 4,832,789

AC United States Patent No. 5,698,902

AD United States Patent No. 4,945,067

AE United States Patent No. 5,441,915

25 AF United States Patent No. 5,698,902

AL European Patent Application No. 0 661 744

AM Patent Abstracts of Japan No. 08 236767

AR D. Widmann, et al., "Technologie hochintegrierter Schaltungen", 2d. Edition, Springer Verlag, pp. 346-347, 1996.

#### II. EXPLANATION OF RELEVANCE

References AA, AB, AC, AD, AE, AF, AL, AM and AR were cited during earlier PCT examination proceedings. A translation of the reference AR, which is in German, is not readily available, however, a summary of its teachings is in the specification. Since references AA, AB, AC, AD, AE, AF, AL and AM are in English, no further commentary concerning their teachings is necessary.

Reference AR was also cited for the reasons noted in the Applicant's specification.

None of the above references discloses or suggests an itegrated circuit arrangement or method for the manufacture thereof as disclosed in the present invention.

Copies of each of the above references together with Form 1449 are submitted herewith in accordance with 37 C.F.R. §1.98. Except as provided, the undersigned does not possess English translations of the non-English references.

This Information Disclosure Statement is being submitted simultaneously with the filing of the present application, and is therefore in compliance with 37 C.F.R. §1.97(b) and no fee is necessary.

All claims of the application are patentable over the teachings of the above references, taken singly or in combination. Early consideration of the application is therefore respectfully requested.

Submitted by,

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Sheet <u>1</u> of <u>1</u>

### 37 CFR 1.501 INFORMATION DISCLOSURE STATEMENT IN A PATENT

(use several sheets if necessary)

Docket No. P99,2666 09 7 4 6 2 9 9

**Applicants** 

Udo Schwalke, et al.

Filing Date Group Art Unit

			U.S. 1 A 1	ENT DOCUMENTS				
Examiner's Initials		Document Number	Date	Name	Class	Subclass	Filing Date If appropriate	
IIIIIII	AA	5,416,343	05-16-95	J. Slotboom, et al.			<u>F</u> F	1
<u>.</u>	AB	4,832,789	05-23-89	W. Cochran, et al.				
	AC	5,698,902	12-16-97	T. Uehara, et al.				
	AD	4,945,067	07-31-90	T. Huang				
	AE	5,441,915	08-15-95	T. Lee				
	AF	-,						
	AG							
	AH							
	1		FOREIGN P	PATENT DOCUMENTS			<u> </u>	
				Country	Class	Subclass	Translation	
		Document Number	Date				Yes	No
	AL	EP 0 661 744 A1	07-05-95	Europe			X	
	AM	JP08-236767	09-13-96	Japan			Abstract	
	AN							
	AO							
	AP			,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,				
	AQ							
		OTHER PRIOR A	RT (Including	Author, Title, Date, Pe	rtinent Pag	es, Etc.)		
	AR	D. Widmann, et al., 347, 1007	"Technologie	hochintegrierter Schaltun	gen", 2d. Ed	lition, Springe	er Verlag, p	op. 346
	AS							
	AT							
	AU							
	AV							
	AW							
	AX							
Examiner			Date Conside	ered			<u></u>	

\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to

applicant.

**U.S. PATENT DOCUMENTS**